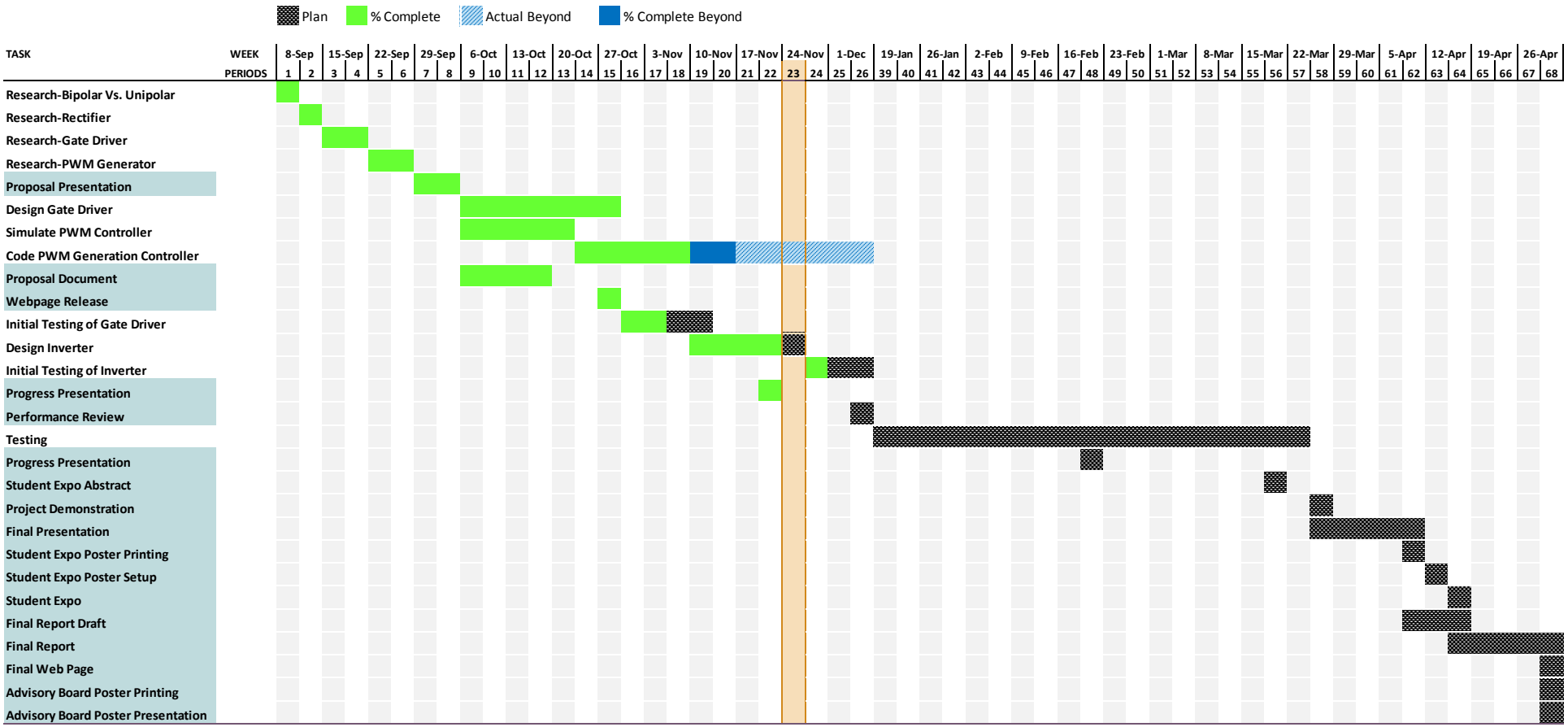
Update: November 24, 2015



Update: November 24, 2015

Progress on the PWM generation controller code is still behind schedule. A problem arose when building the code as it was too large of an array. Using flash memory is what Eric will be researching over Thanksgiving break as well as a digital to analog converter. Eric was not able to output onto the oscilloscope for this week and plans for outputting have been pushed back. Further design of the inverter has been accomplished, and Leslie has begun initial testing to the DC to AC voltage inverter. By next week, Leslie plans to have more of the initial testing completed for the gate driver.