

This is the final update of the semester for this project. The RF to DC converter has been designed and manufactured. From simulation results Dr. Shastry and myself found it necessary to design two separate systems. One system includes a matching network, while the other does not. In simulation the system without the matching network outperformed the system with the matching network. In testing the system without the matching network performed well. The system was able to take a 5.8 GHz RF input and convert it to a DC output. The output appears to have ripple is much lower than the 1% tolerance allotted for in this project. The system is also small in size; less than three square inches. Overall the project was a success and leaves room for students to expand upon it in several different directions.

ID	Task Name	Start	Finish	Duration	Spring 2015 - Spring 2016						
1	Research	Spring 2015	Spring 2016	40 Weeks							
2	Diode Selection	Fall 2015	Fall 2015	1 Week							
3	Diode Configuration	Fall 2015	Mid Fall 2015	6 Weeks							
4	Filter Design	Mid Fall 2015	Mid Fall 2015	6 Weeks							
5	Impedance Matching	Mid Fall 2015	End Fall 2015	6 Weeks							
6	Purchase Parts	End Fall 2016	End Fall 2015	1 Day							
7	Circuit Implementation	Beginning Spring 2016	Mid Spring 2016	12 Weeks							
8	Contact Manufacturer	Mid Spring 2016	End Spring 2016	1 Week							
9	Test Product	End Spring 2016	End Spring 2016	3 Weeks							